

Intel Docket No: P15914 (2112-021)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

BURTON, E. et al.

Serial No. 10/607,550

Filed: June 27, 2003

: Assignee: Intel Corporation

: Confirmation No. 7414

: Examiner: Tran, L.

: Group Art Unit: 2818

For: ADDITION OF METAL LAYERS WITH SIGNAL REALLOCATION TO A
MICROPROCESSOR FOR INCREASED FREQUENCY AND LOWER POWER

AMENDMENT

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
401 Dulany Street
Alexandria, Virginia 22314

Sir:

The following amendments and remarks are submitted in response to the Office

Action mailed on January 31, 2005 in connection with the above-identified application.

Amendments to the Claims are in the Listing of Claims beginning on page 2.

Amendments to the Drawings are discussed on page 5.

Remarks begin on page 6.

Do Not Enter

Thanks,

dhin

04/05/06